



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	
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YASUDA et al.)	
)	Unit 2111
Application Number: 10/654,983)	
)	Examiner
Filed: September 5, 2003)	Ray, Gopal C.
)	
For: SHARED MEMORY MULTIPROCESSOR)	
PERFORMING CACHE COHERENCE)	
CONTROL AND NODE CONTROLLER)	
)	
Attorney Docket No. ASAM.0092)	

**Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231**

RESPONSE AND AMENDMENT UNDER 37 C.F.R. § 1.111

Sir:

This is in response to the Office Action dated September 13, 2004, the period for response to which expires on December 13, 2004. Please amend the above identified application as follows: